

SINGLE-INPUT, DIFFERENTIAL OUTPUT LOW NOISE AMPLIFIER

ABSTRACT

An amplifier has an input terminal to receive an input signal. The amplifier includes a

5 first gain stage comprising a pair of input transistors and a second gain stage to drive an output stage. The output stage provides inverting and non-inverting differential output signals on inverting and non-inverting output nodes. The amplifier may also include a feedback signal electrically connected between the inverting and non-inverting output nodes to emitters of the input transistors through a resistor network.

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